

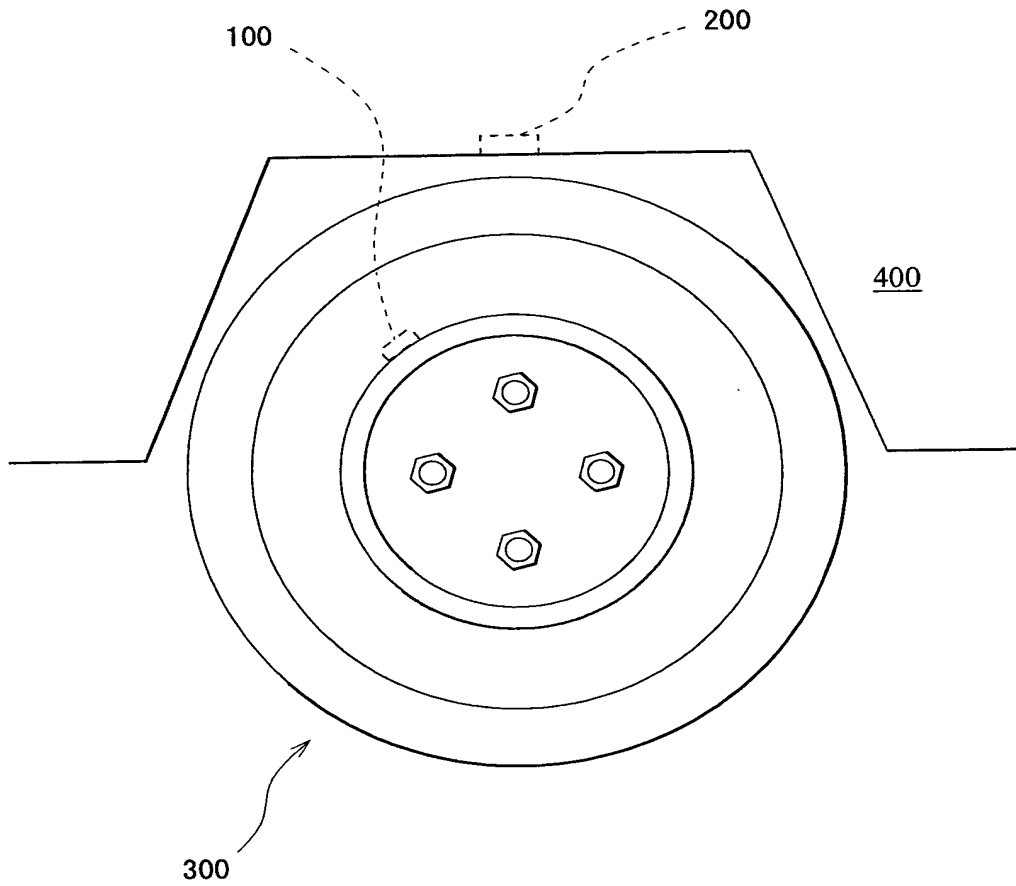
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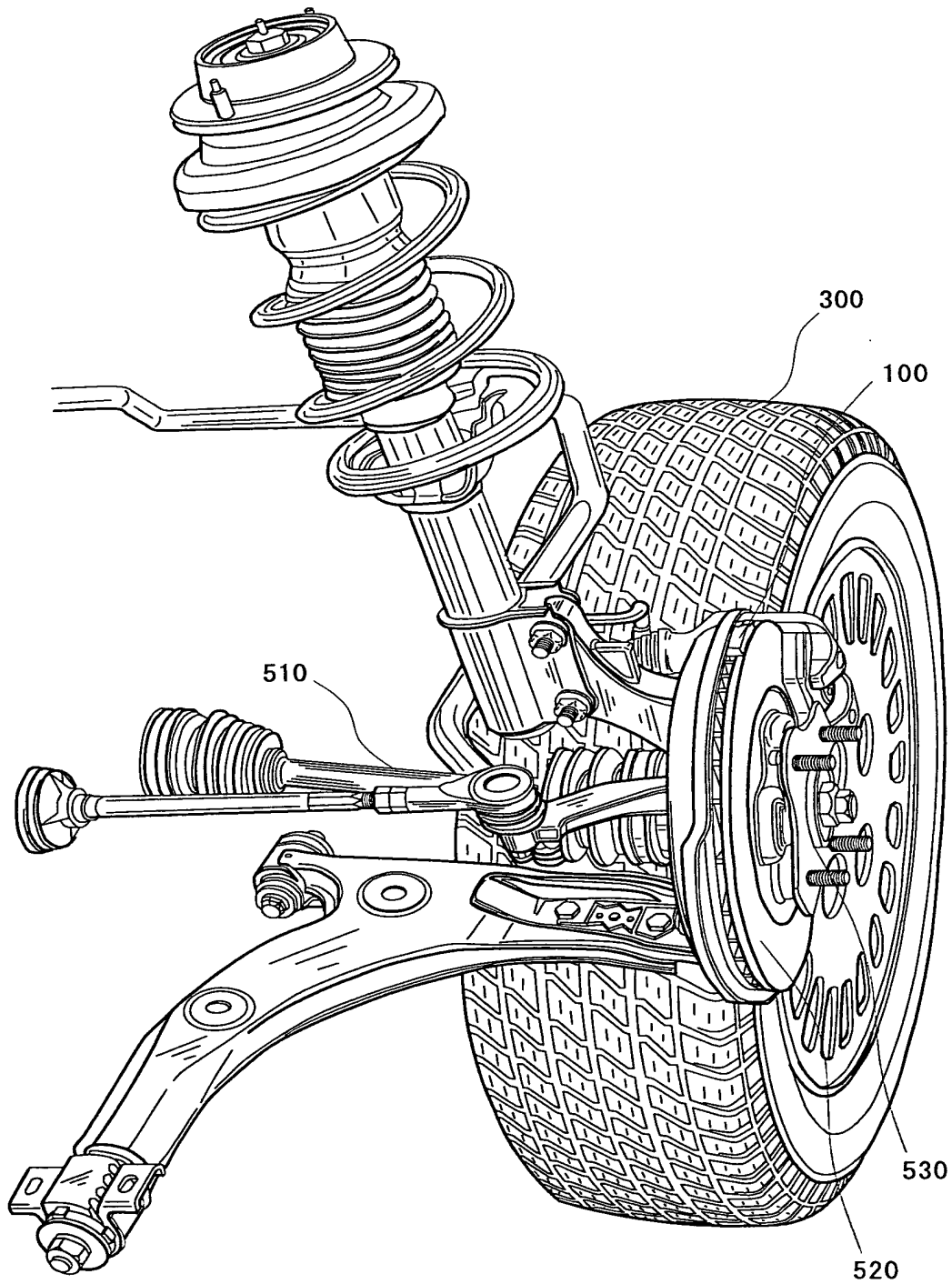
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Fig. 2



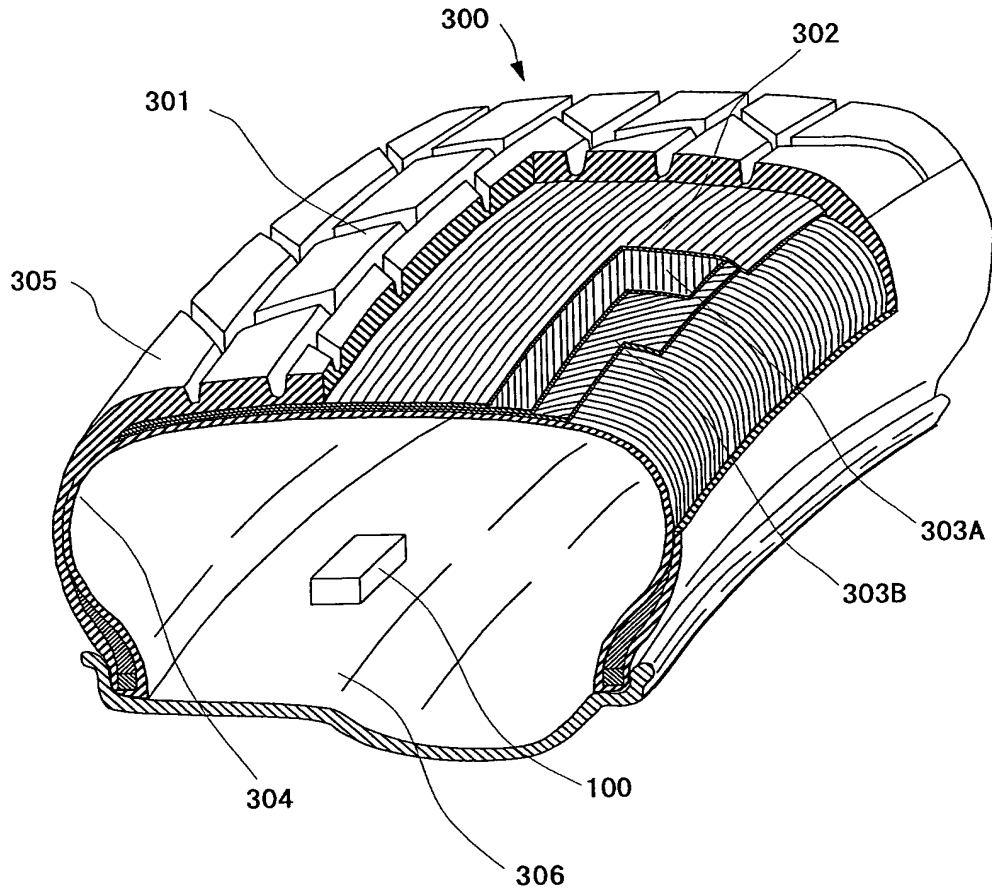
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Fig. 3



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Fig. 4

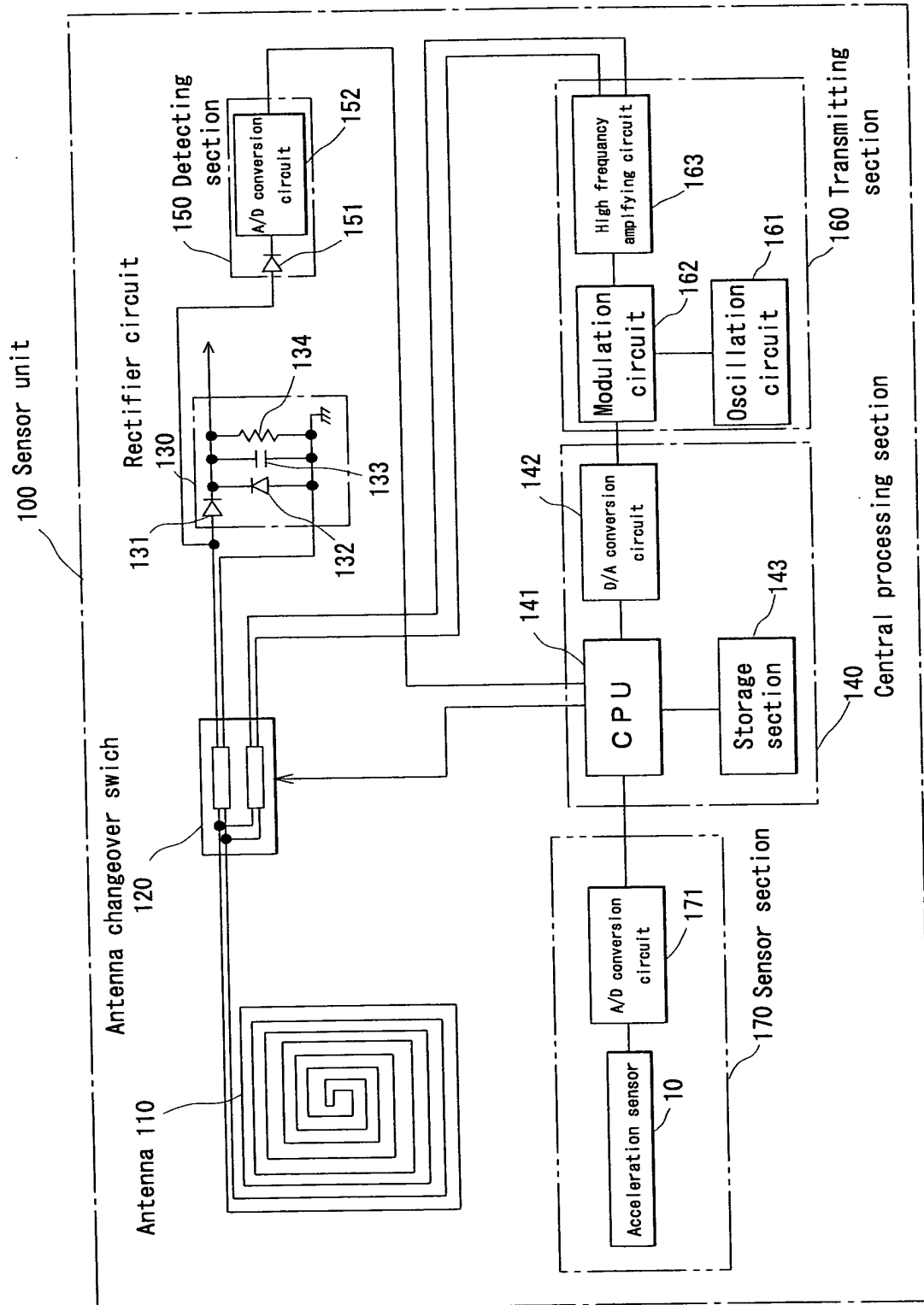


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Fig. 5



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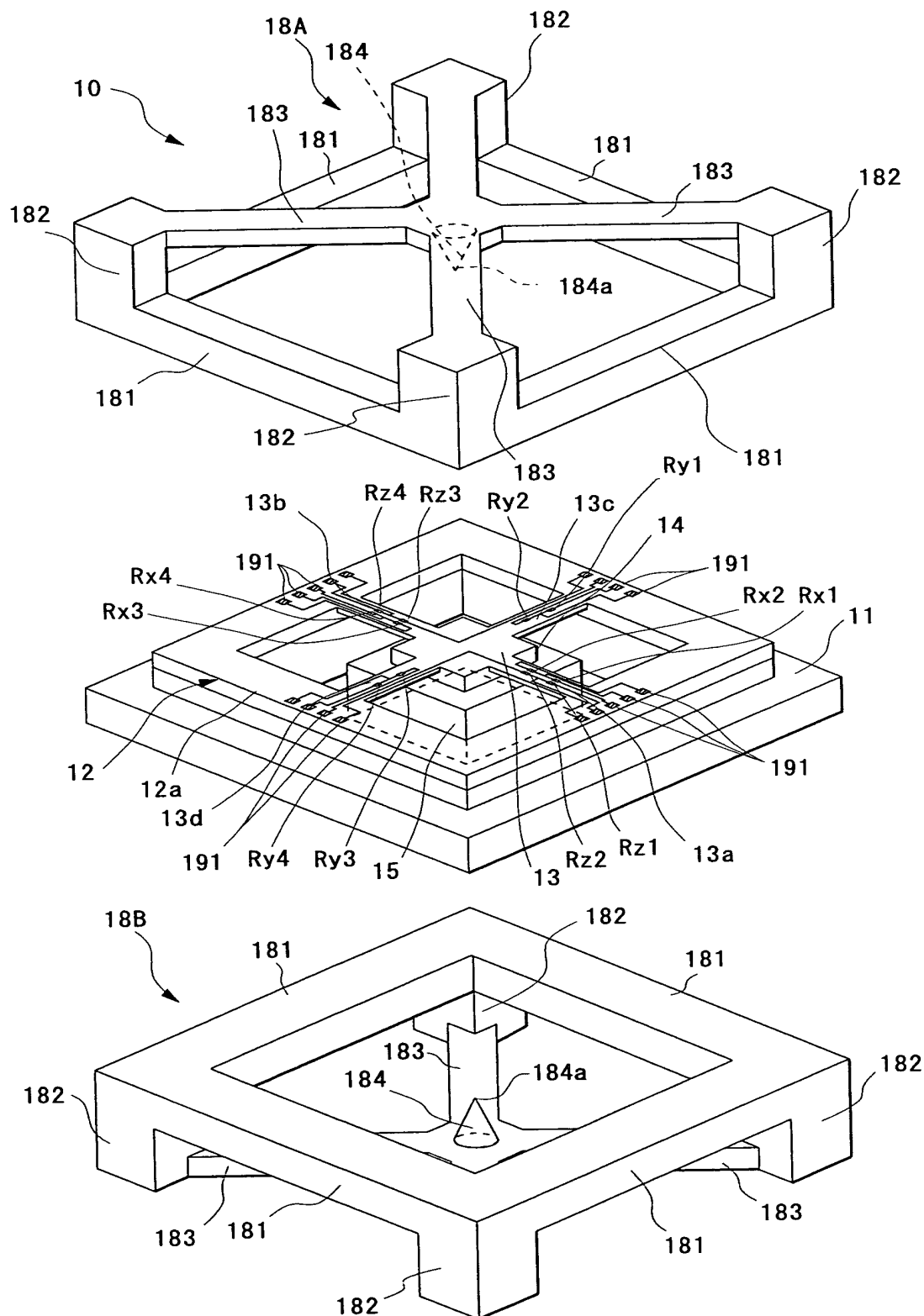
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Figure 1 is a cross-sectional view of a semiconductor device 10. The device includes a substrate 11. A top layer 12 is formed on the substrate 11, and a bottom layer 14 is formed on the substrate 11. The top layer 12 contains regions Rx1, Rx2, Rx3, and Rx4. The bottom layer 14 contains regions 13a and 13b. A central region 15 is defined by a dashed line and arrows D1. Various other regions and layers are labeled with numbers 10, 11, 12, 13a, 13b, 14, 15, 181, 182, 183, 184, and 184a.

[illegible]

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Fig. 9



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Fig. 10

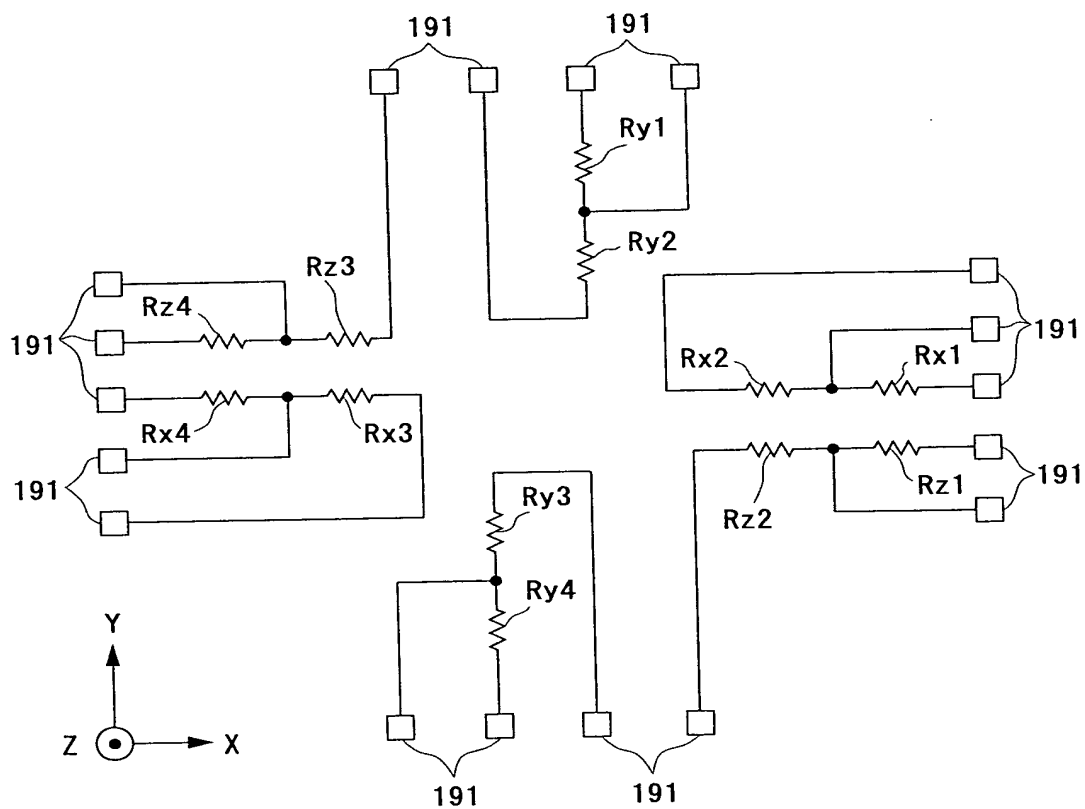
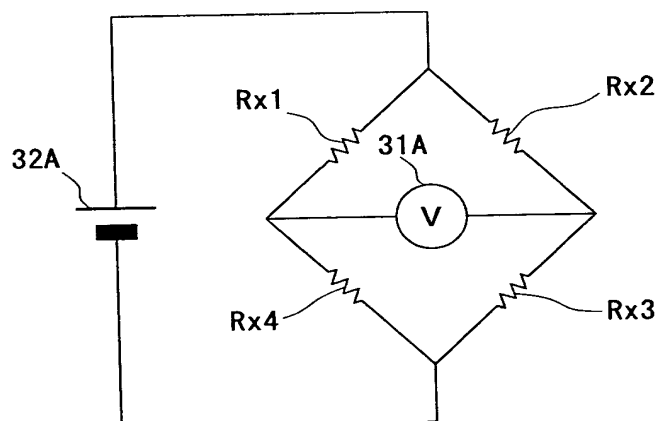


Fig. 11



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Fig. 12

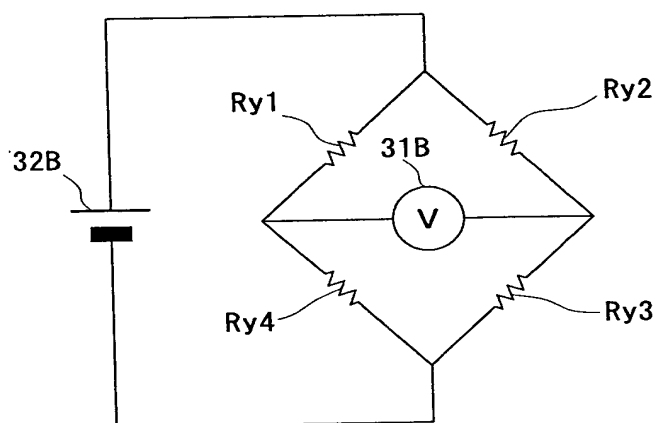
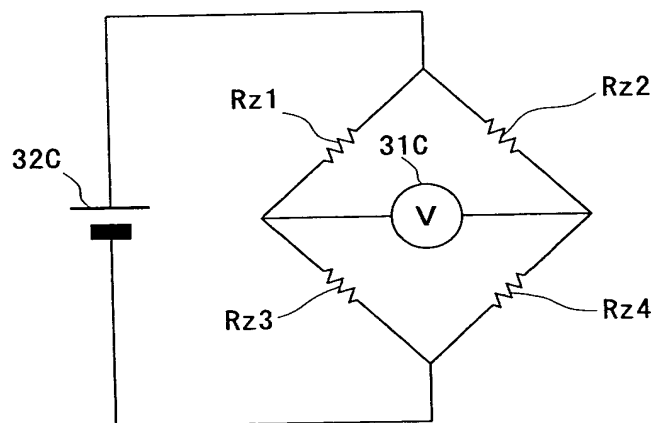
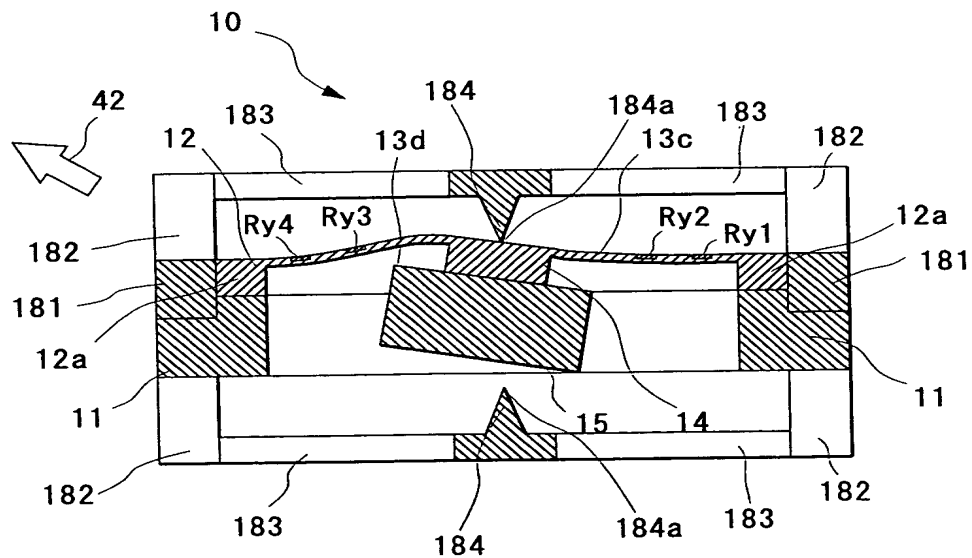
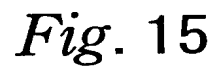


Fig. 13



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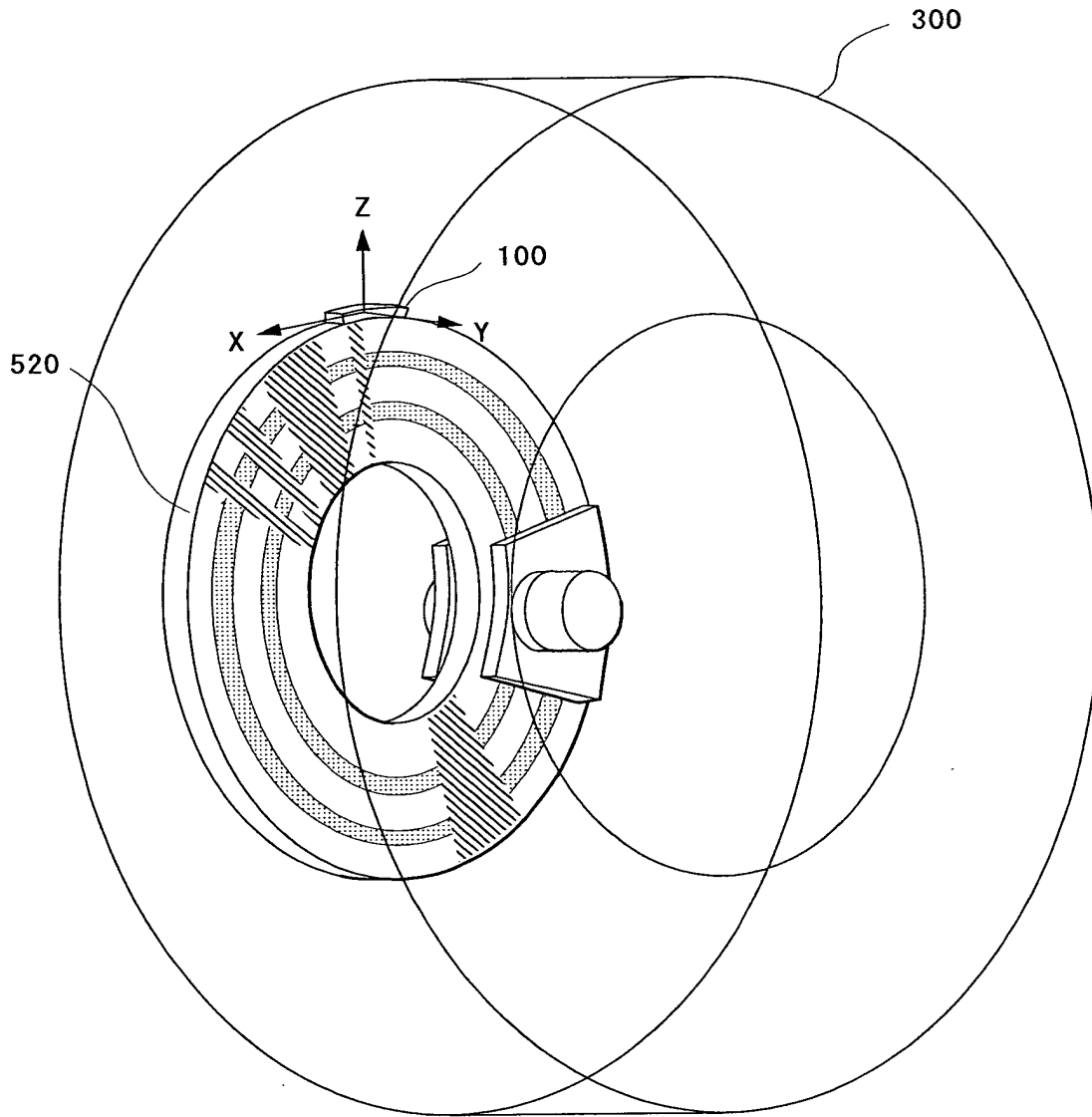
Fig. 14



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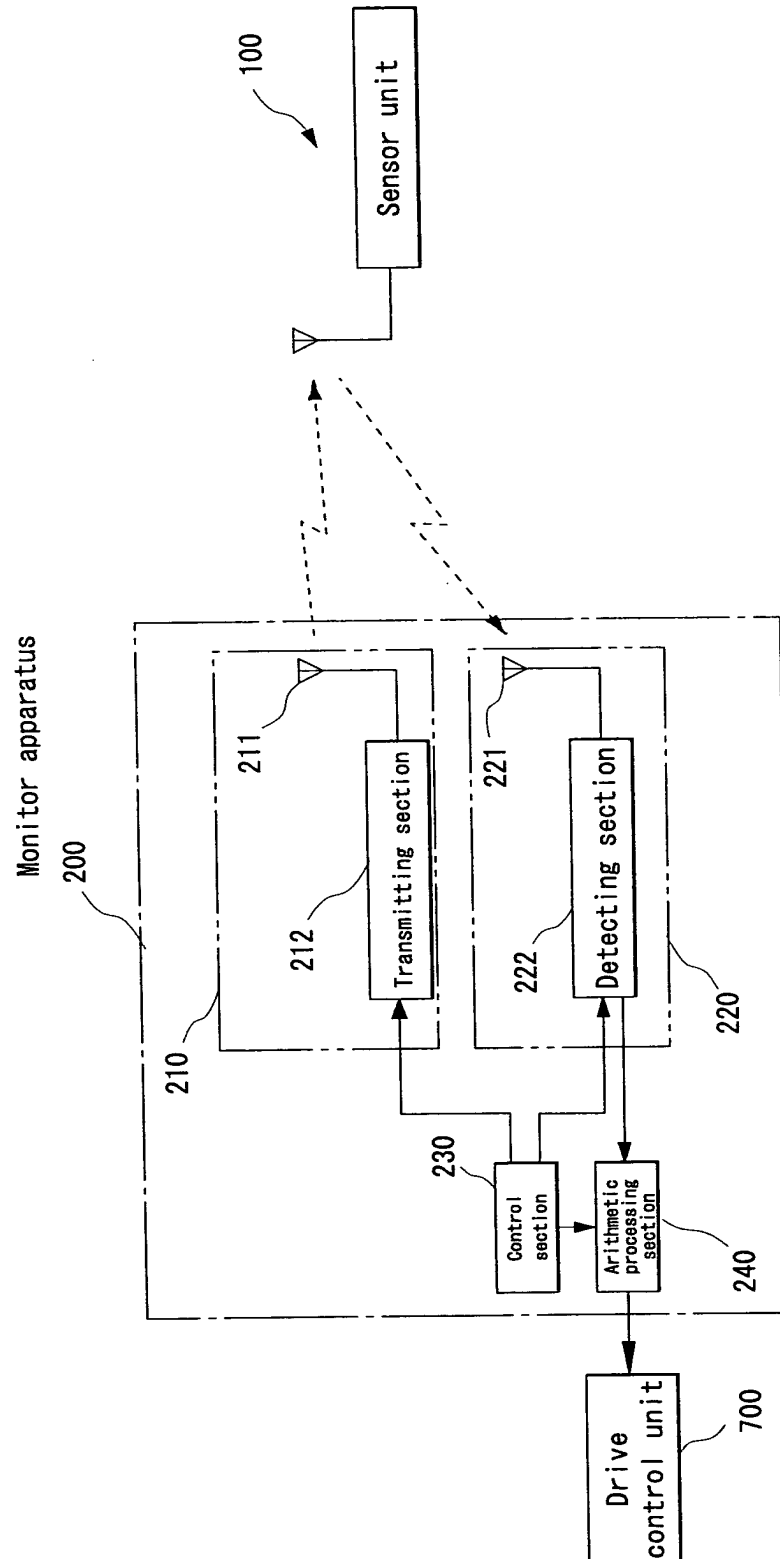
Fig. 16



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Fig. 17

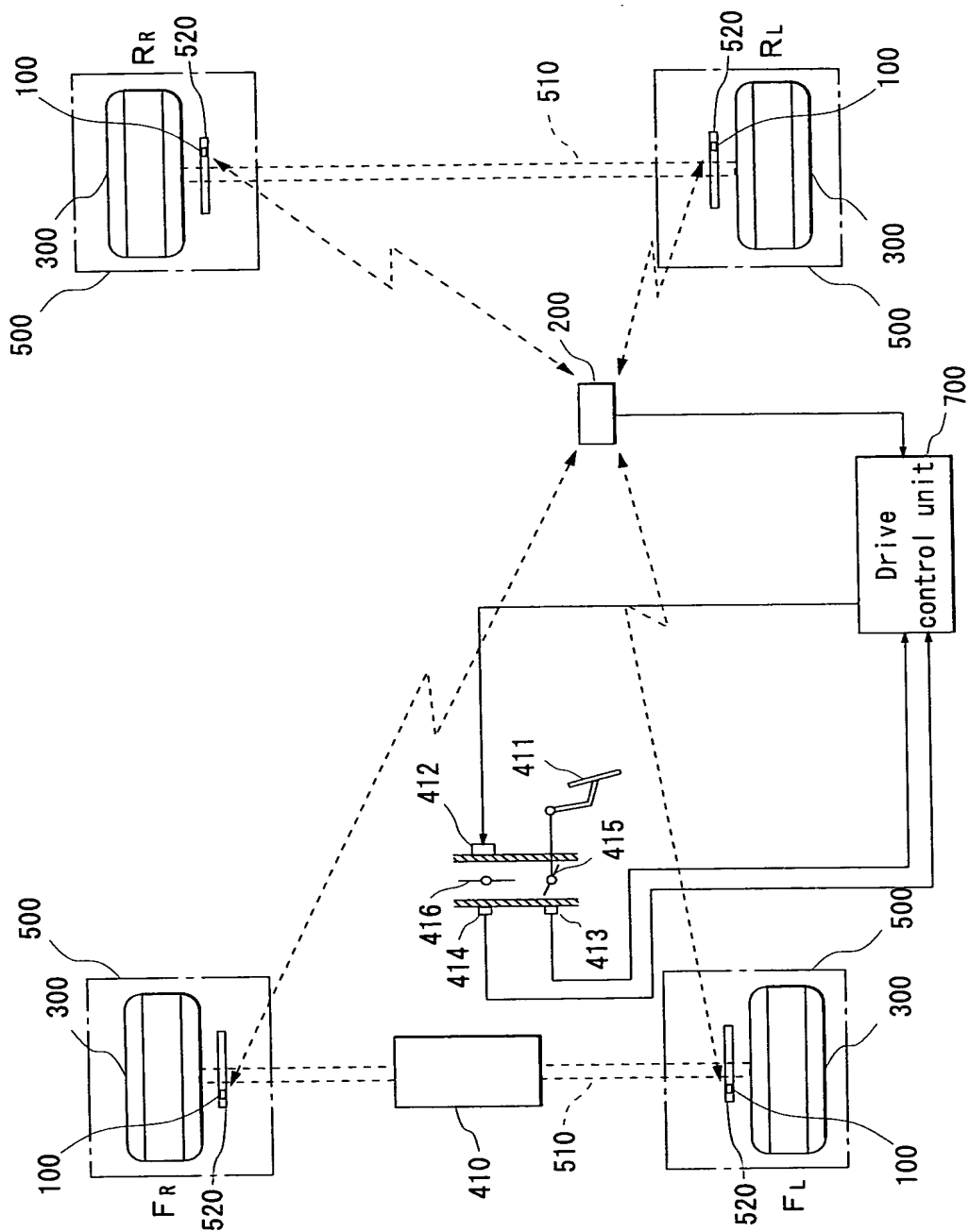


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Fig. 18



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Fig. 19

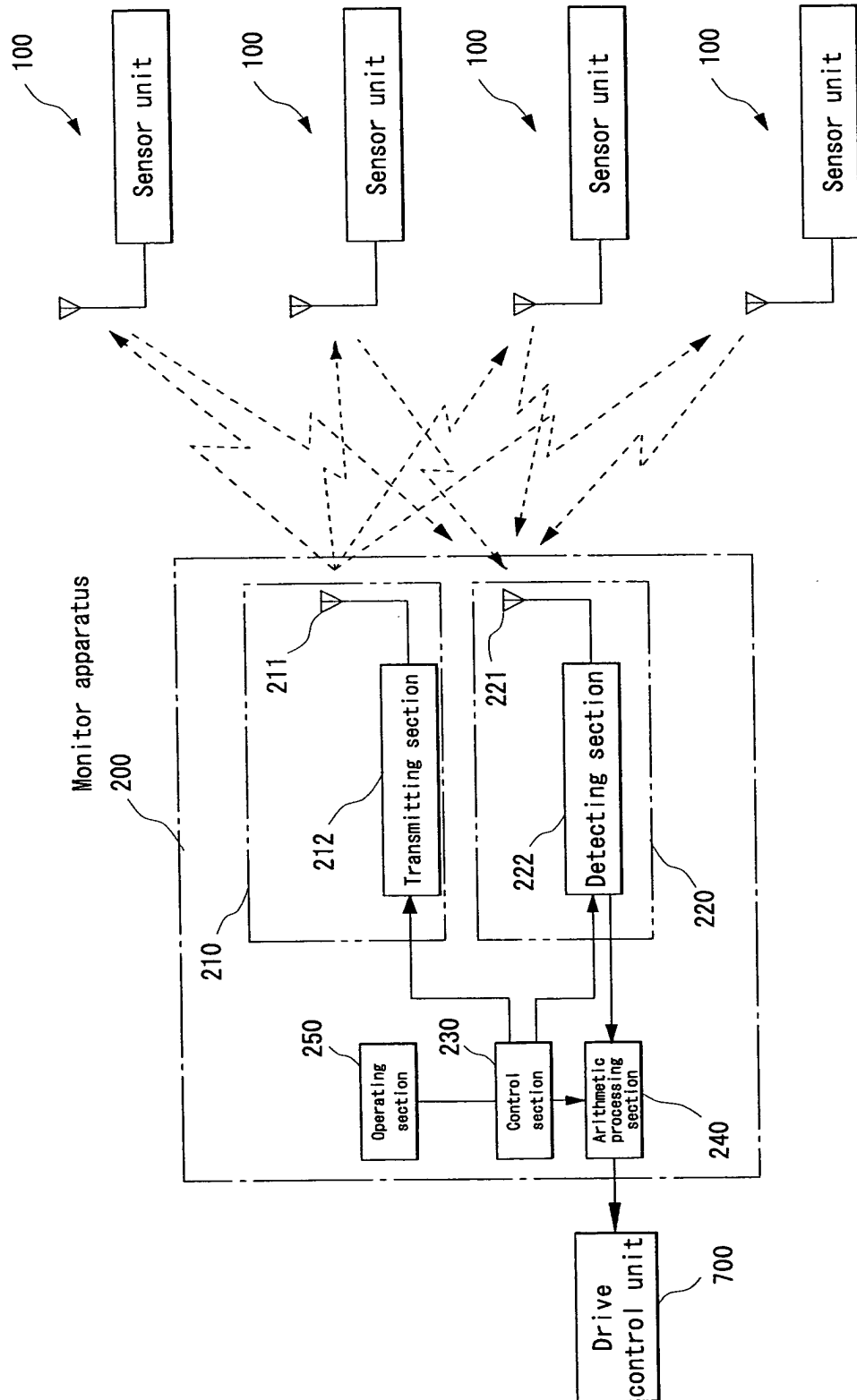


Fig. 20

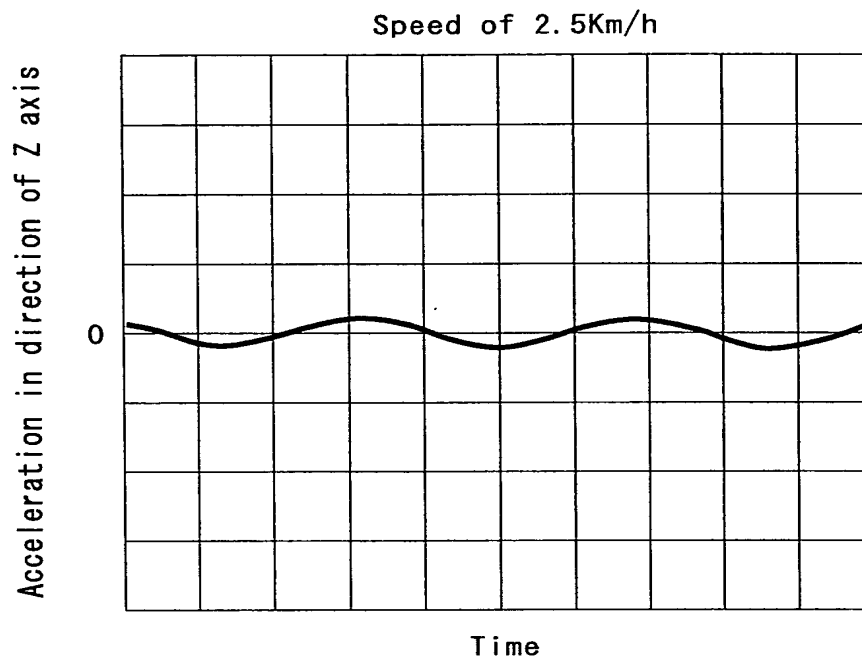
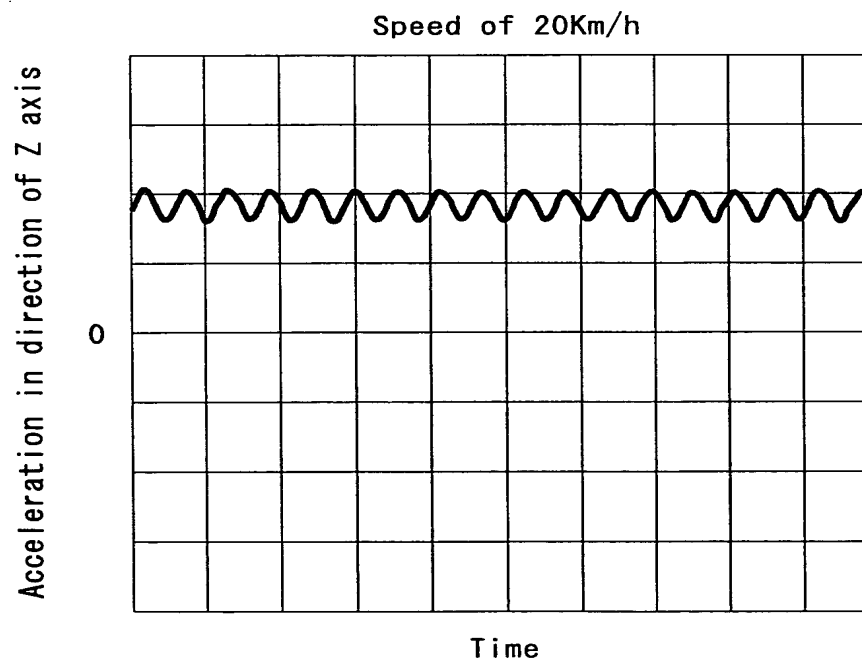


Fig. 21



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Fig. 22

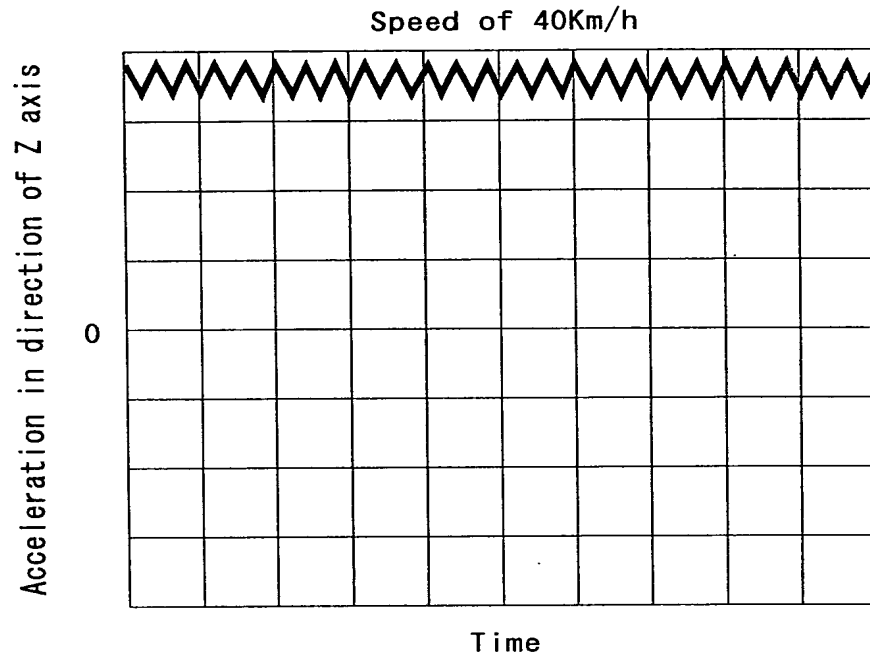


Fig. 23

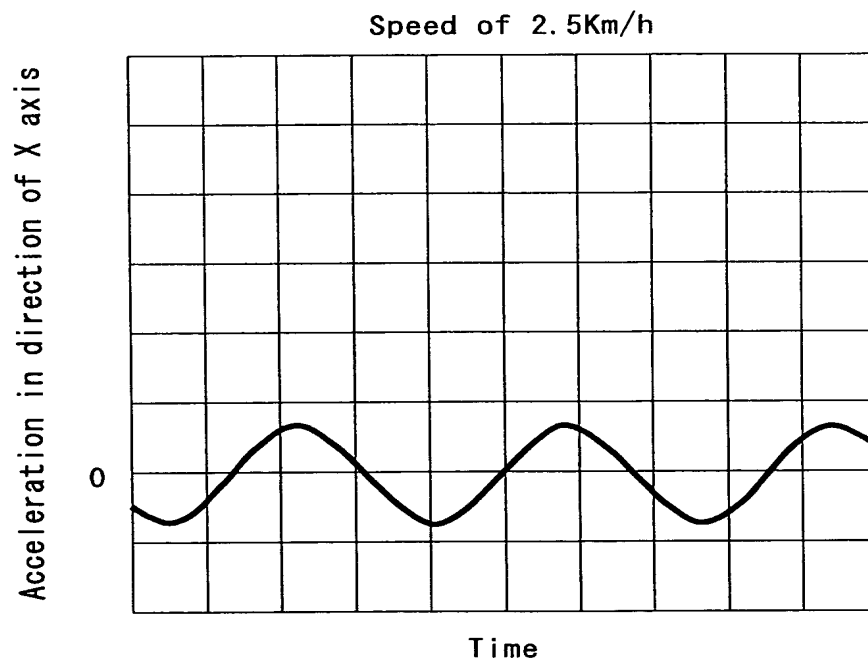


Fig. 24

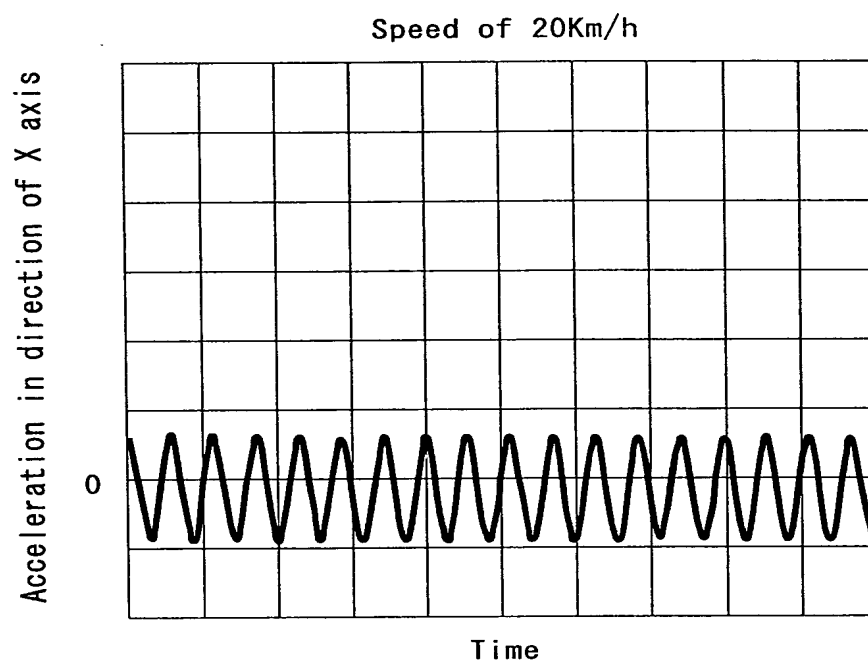
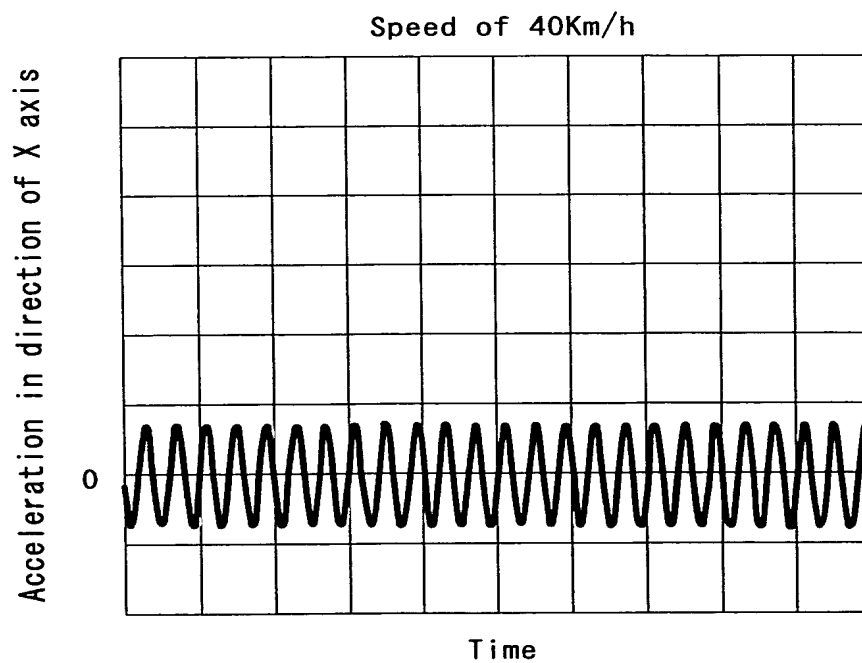


Fig. 25



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Fig. 26

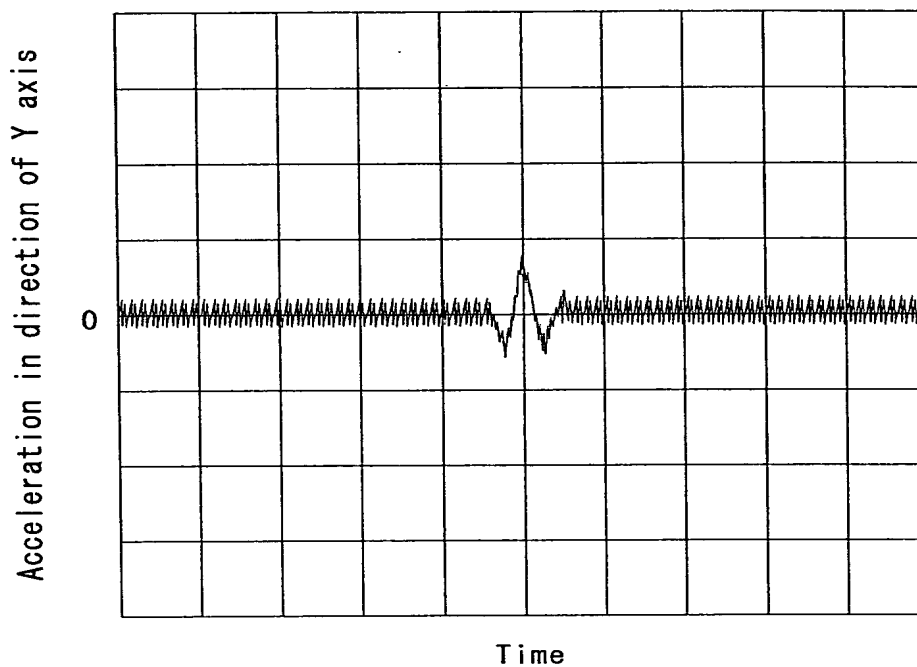
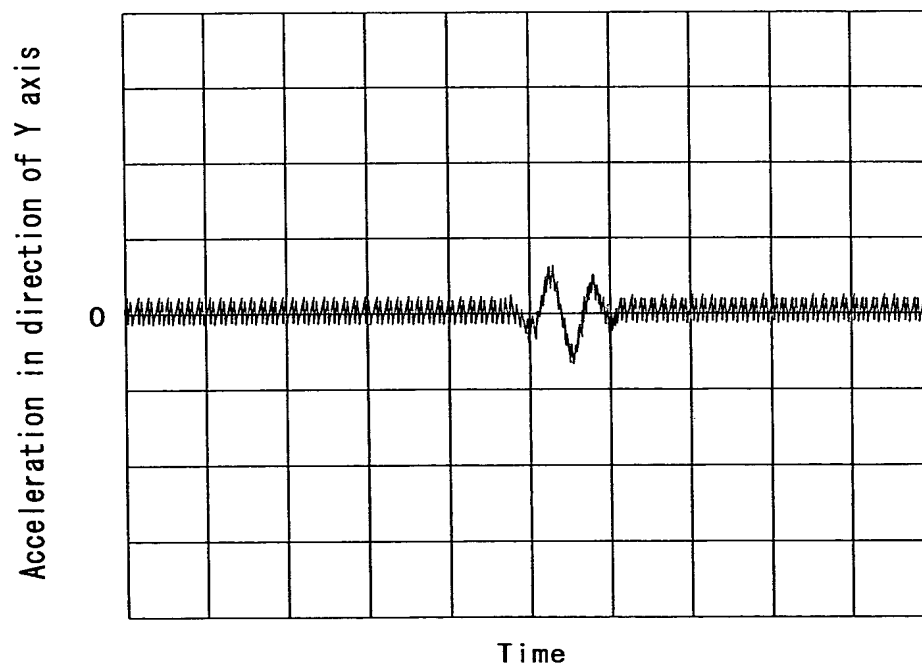


Fig. 27



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Fig. 28

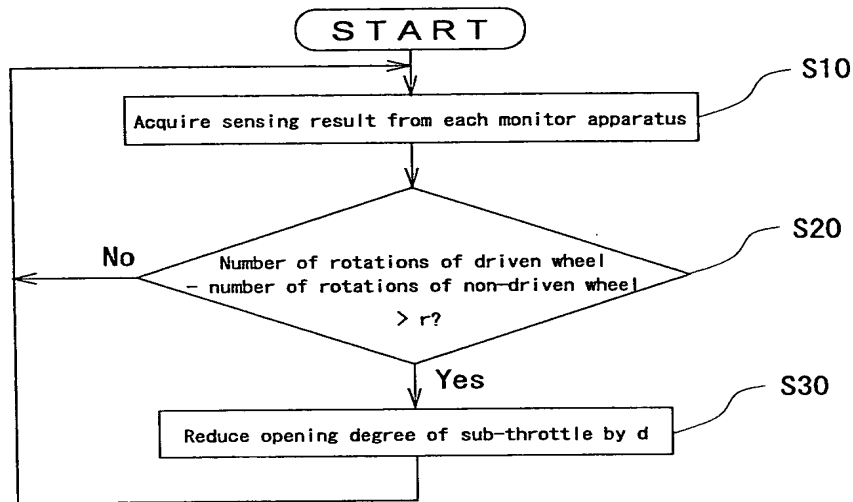
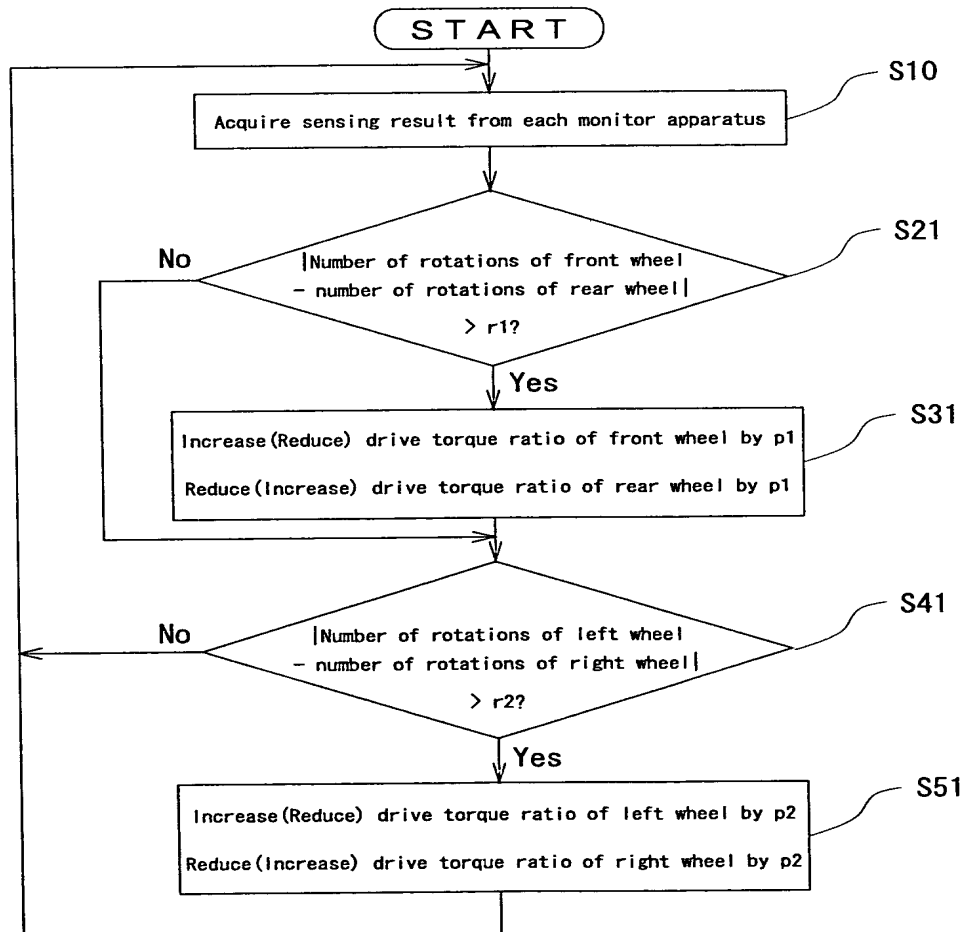


Fig. 29

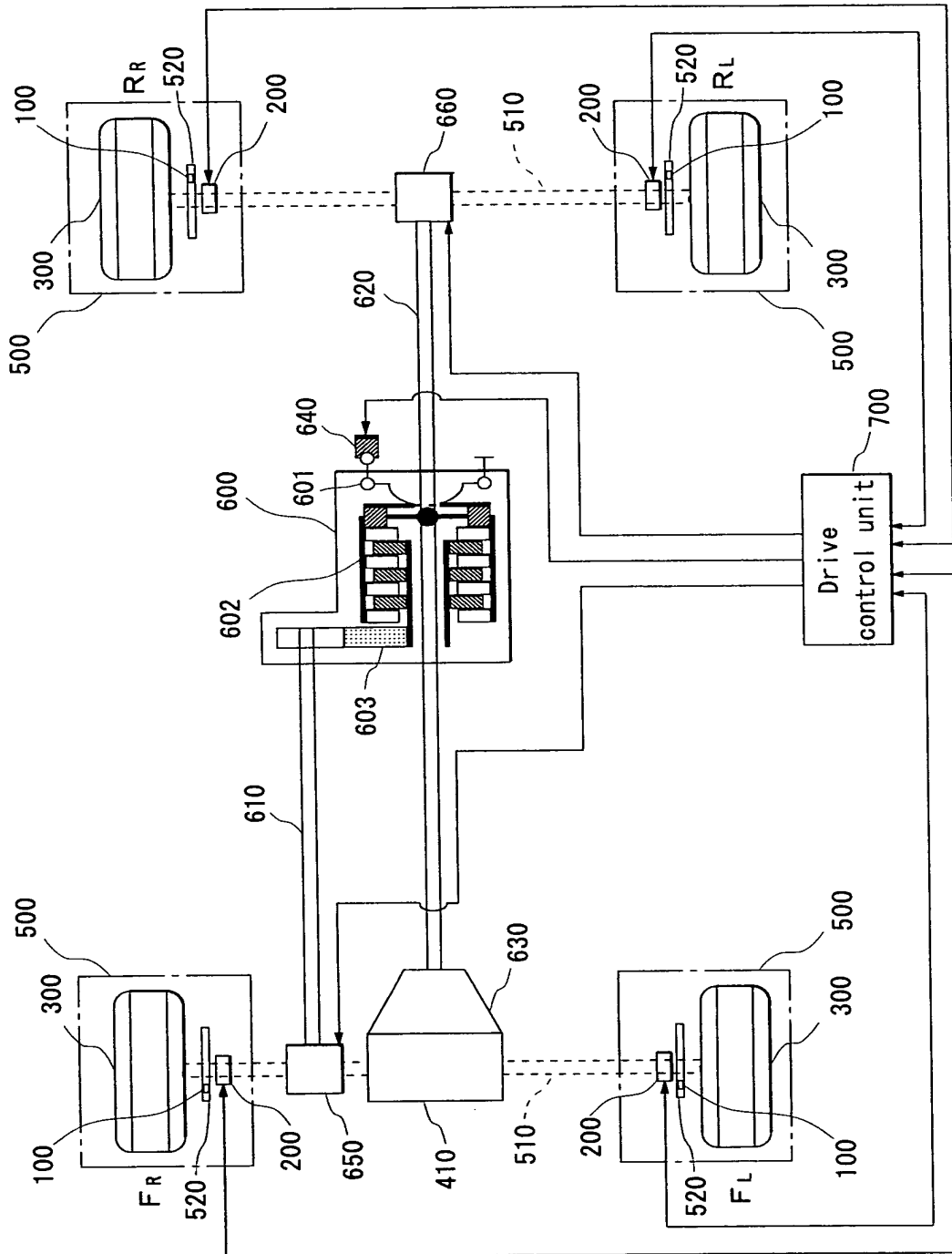


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Fig. 30

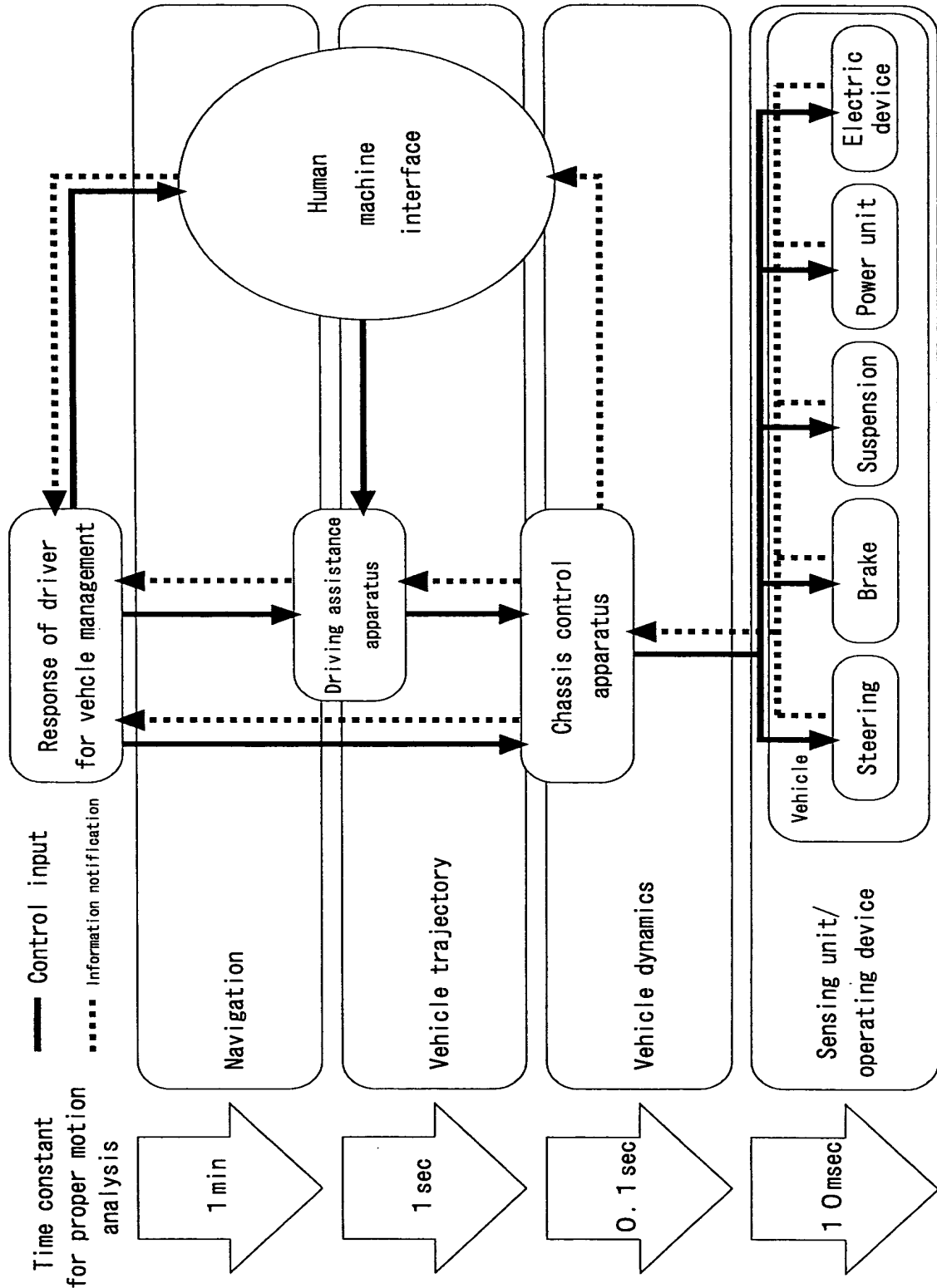


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Fig. 31



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Fig. 32

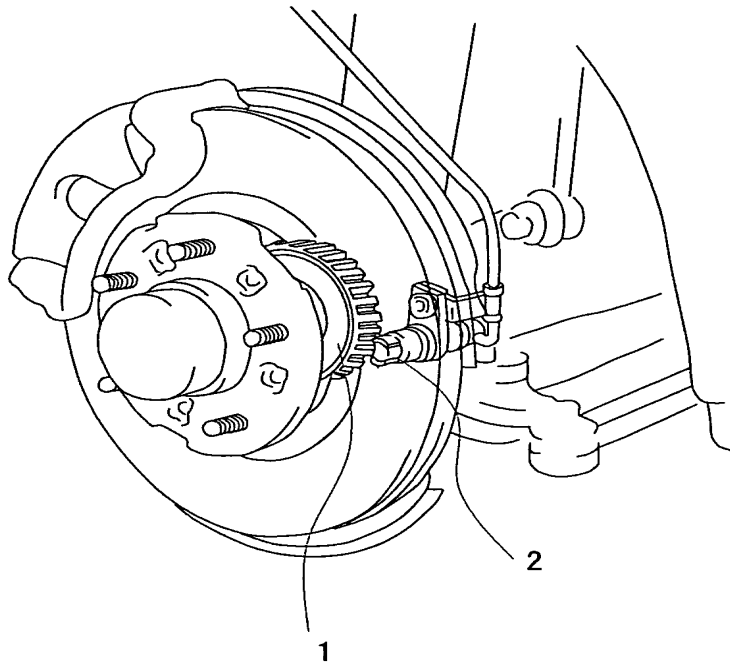
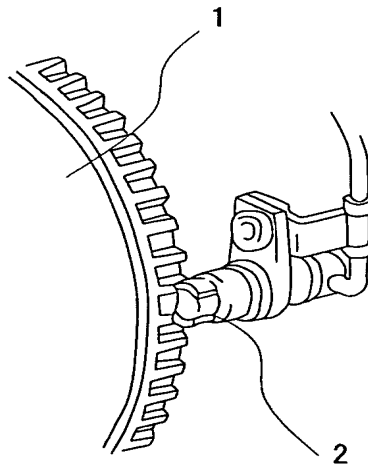
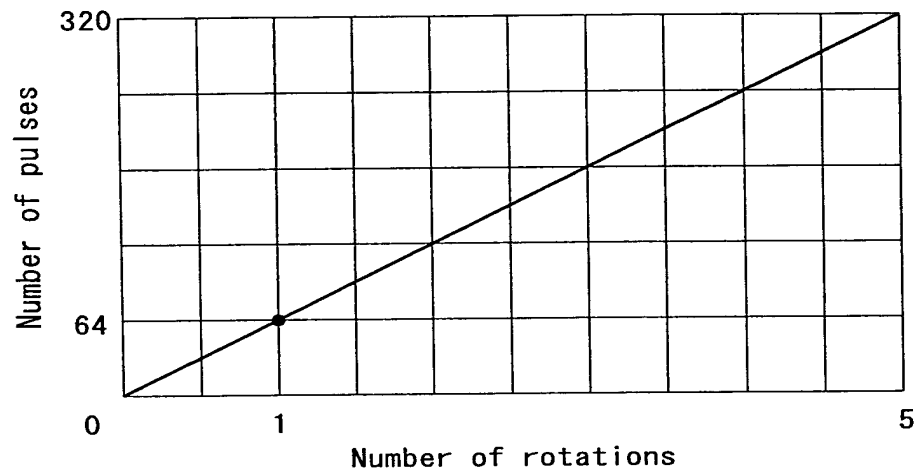


Fig. 33



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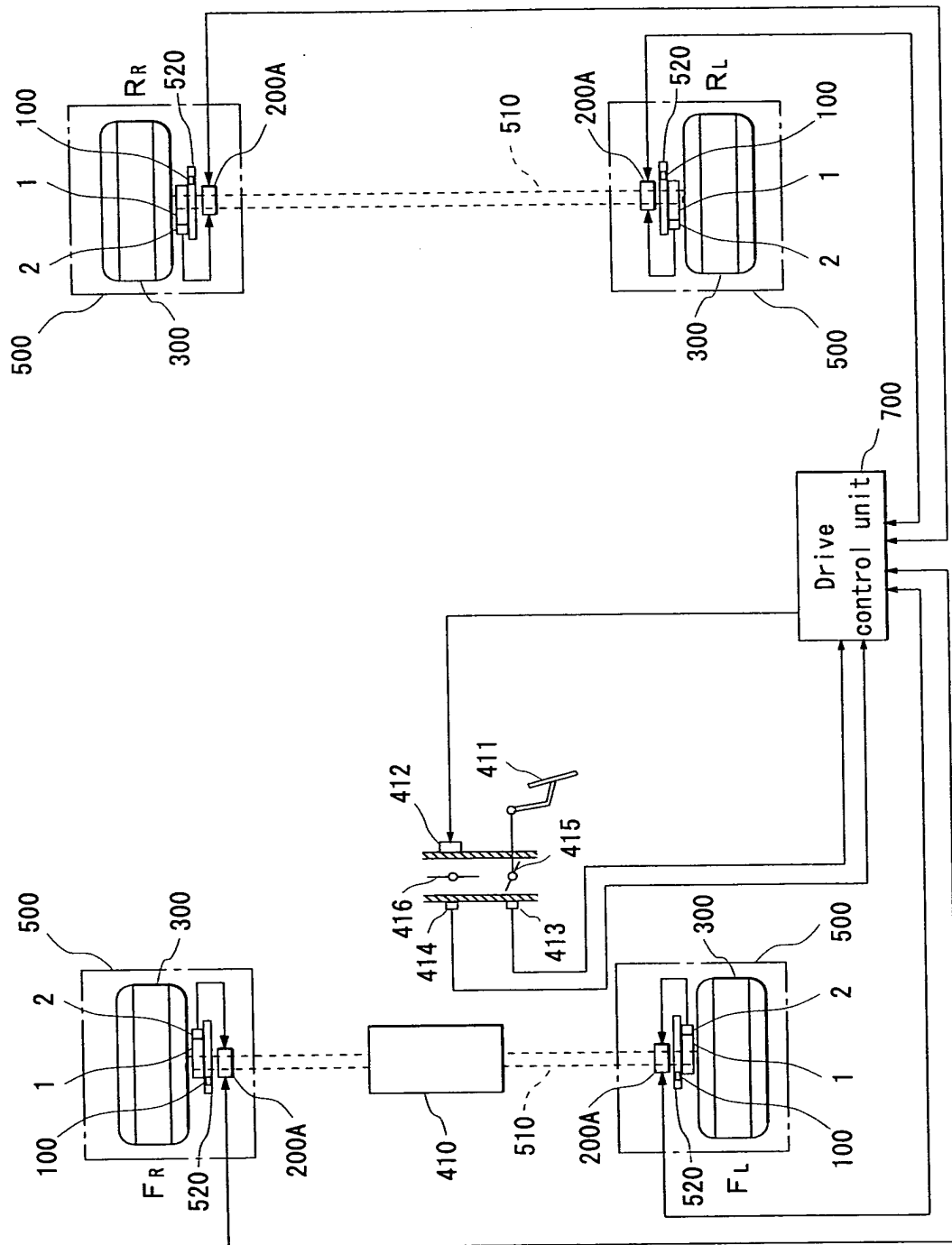
Fig. 34



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Fig. 35

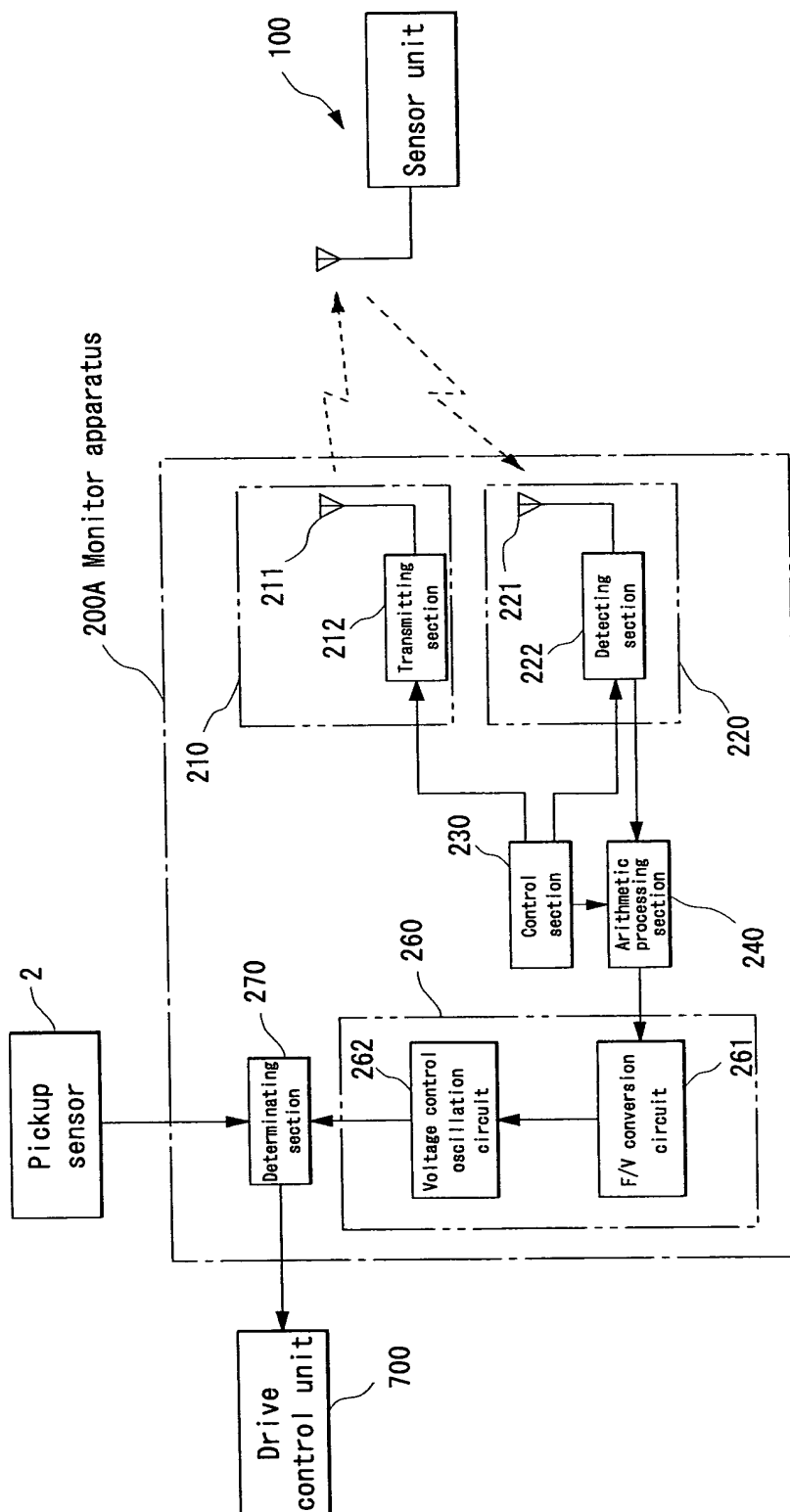


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Fig. 36



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Fig. 37

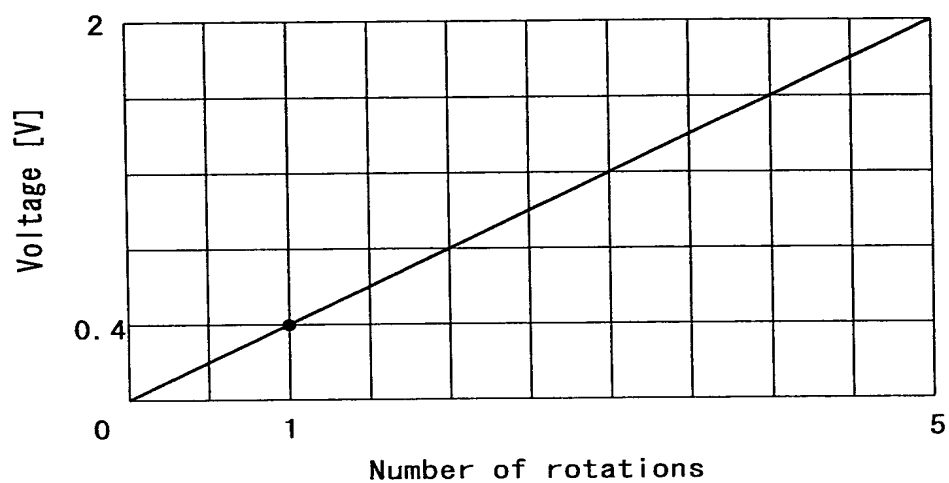
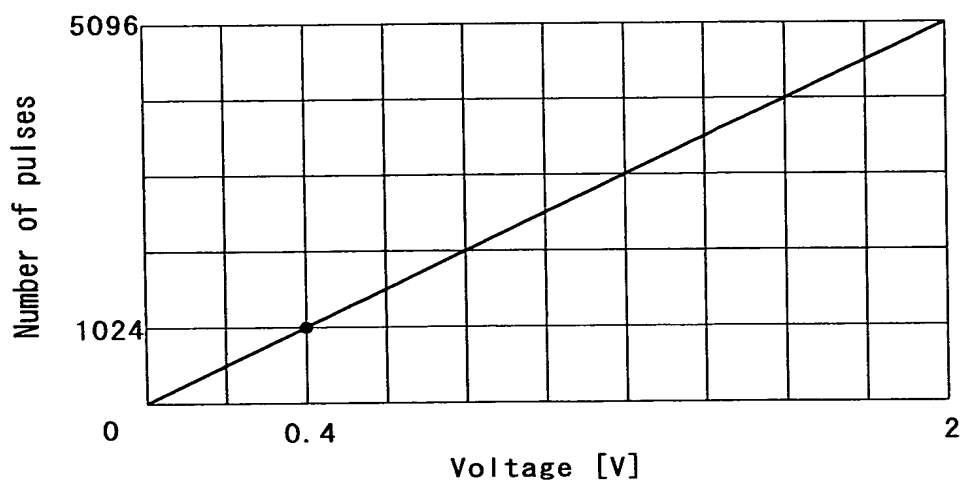


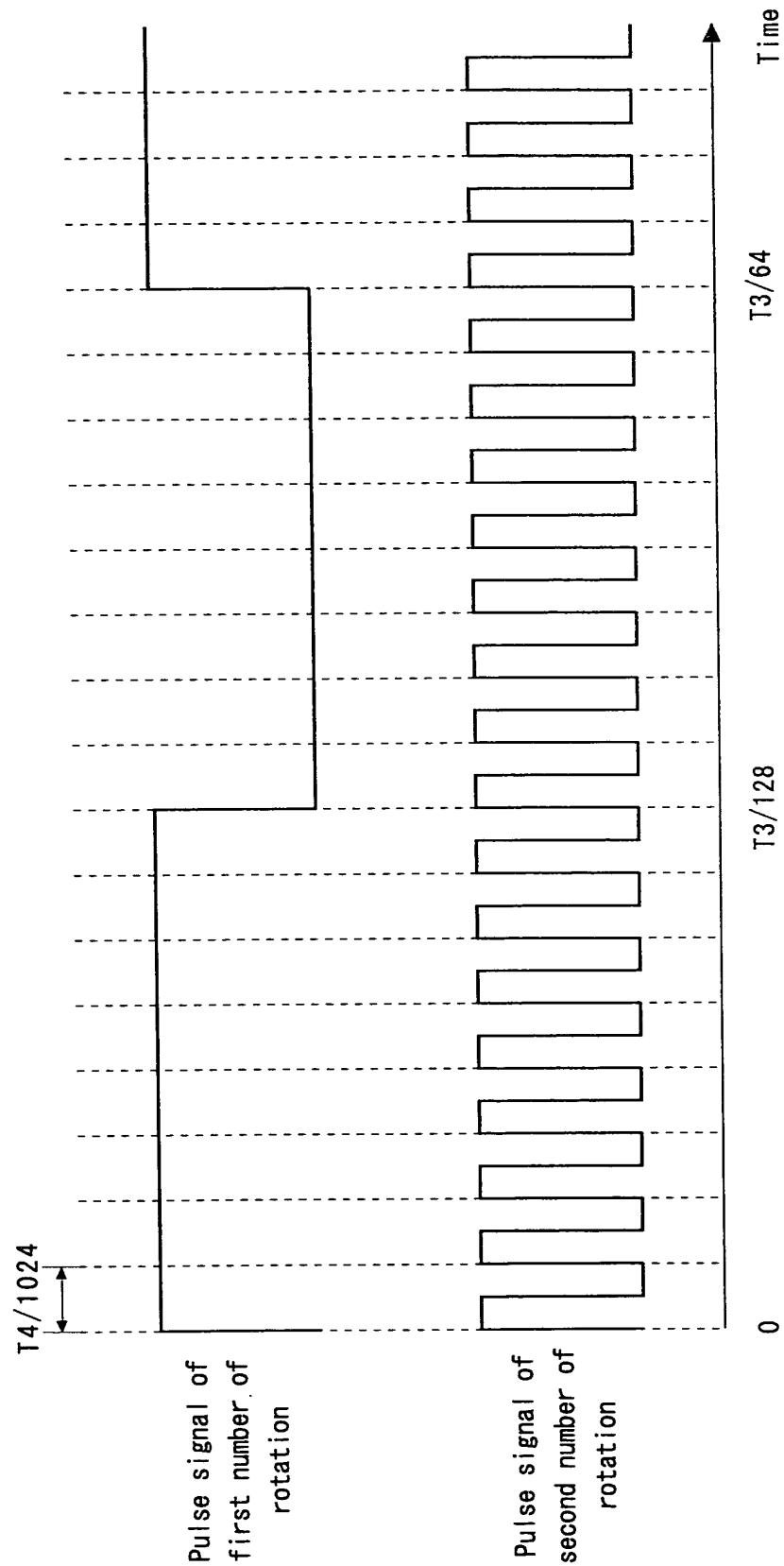
Fig. 38



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Fig. 39



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Fig. 40

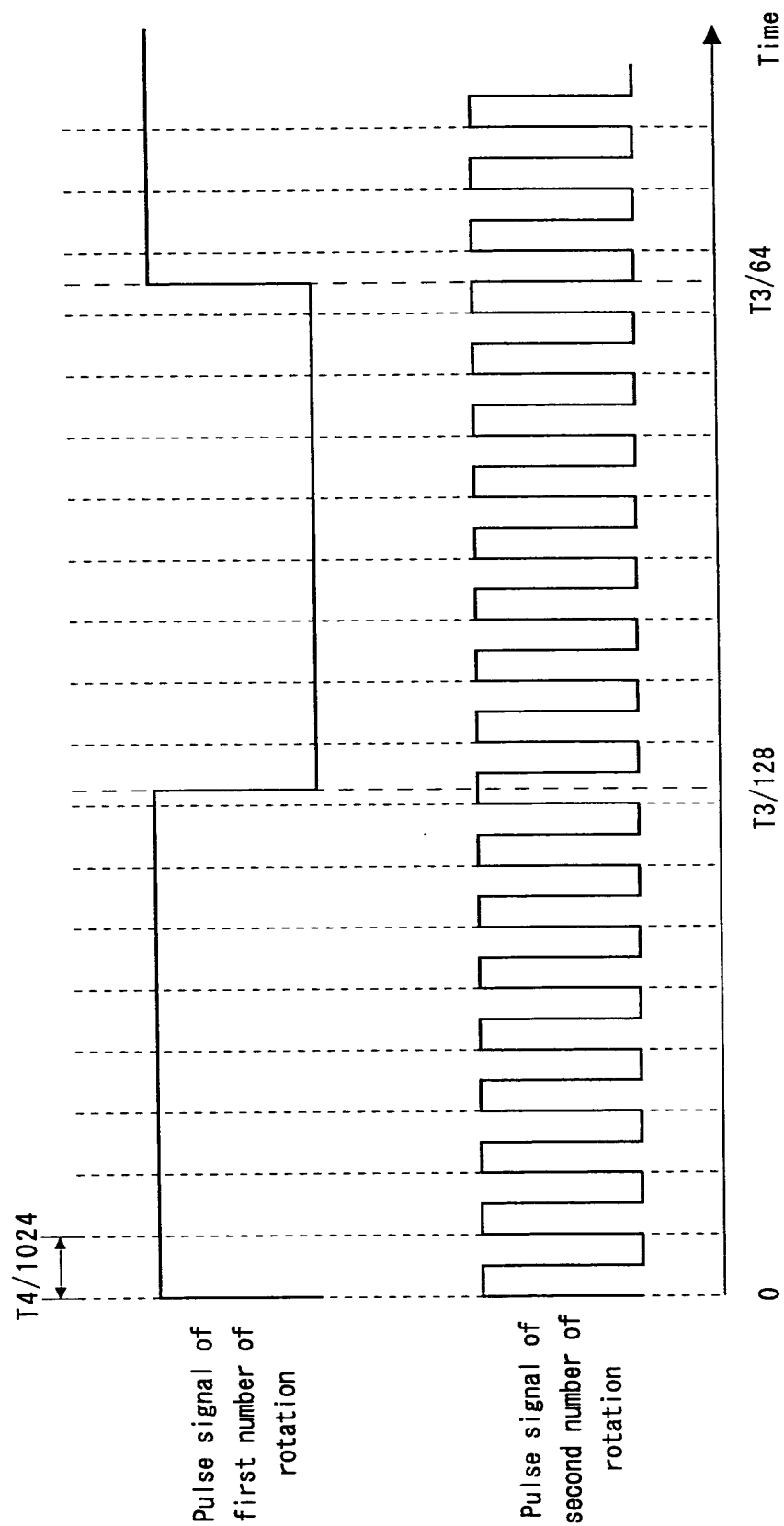
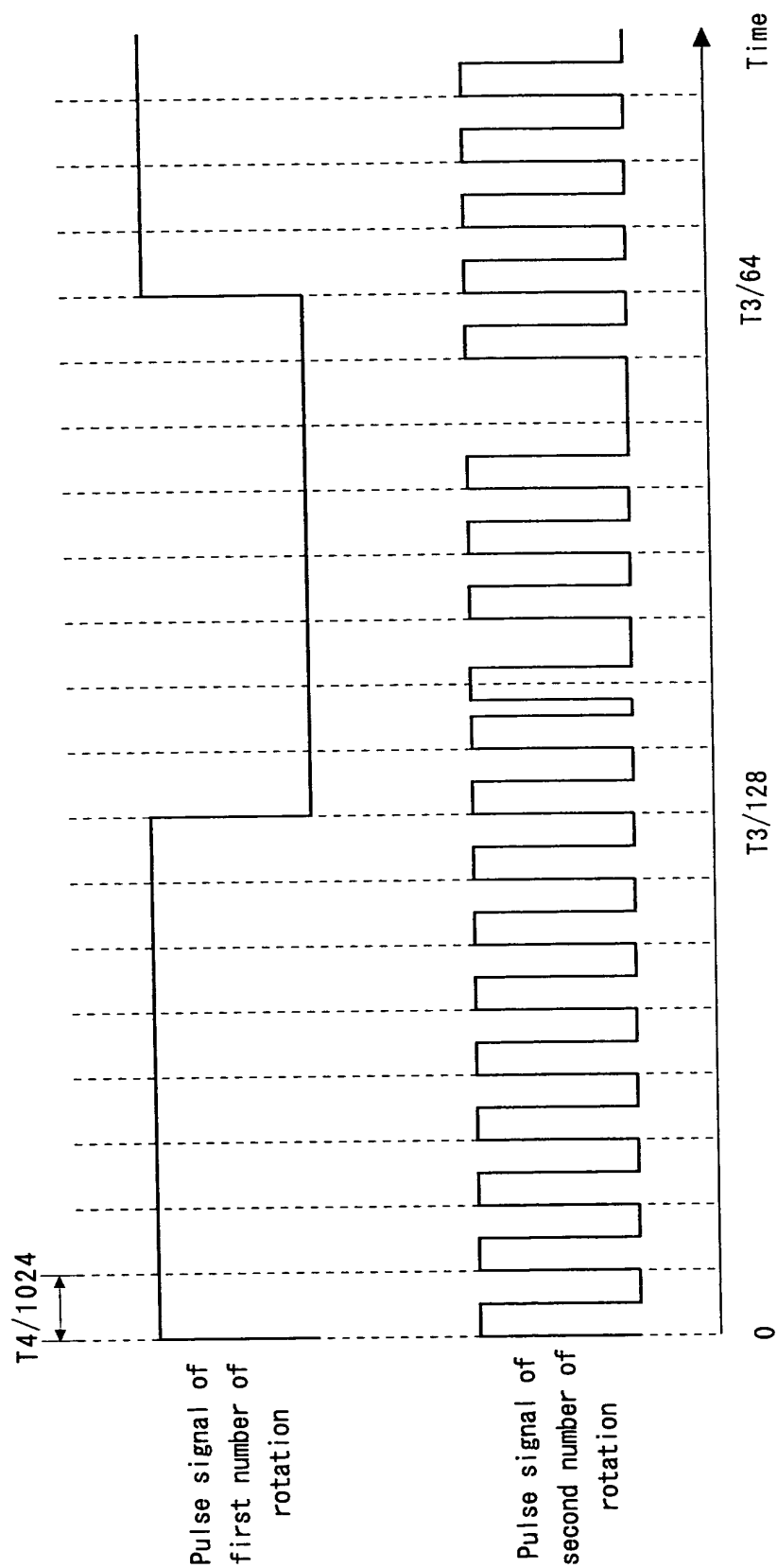
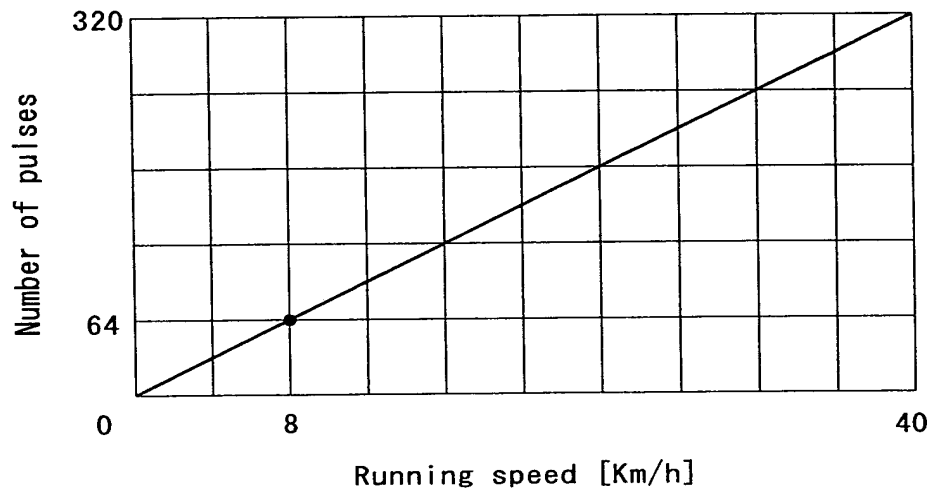


Fig. 41



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Fig. 42



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Fig. 43

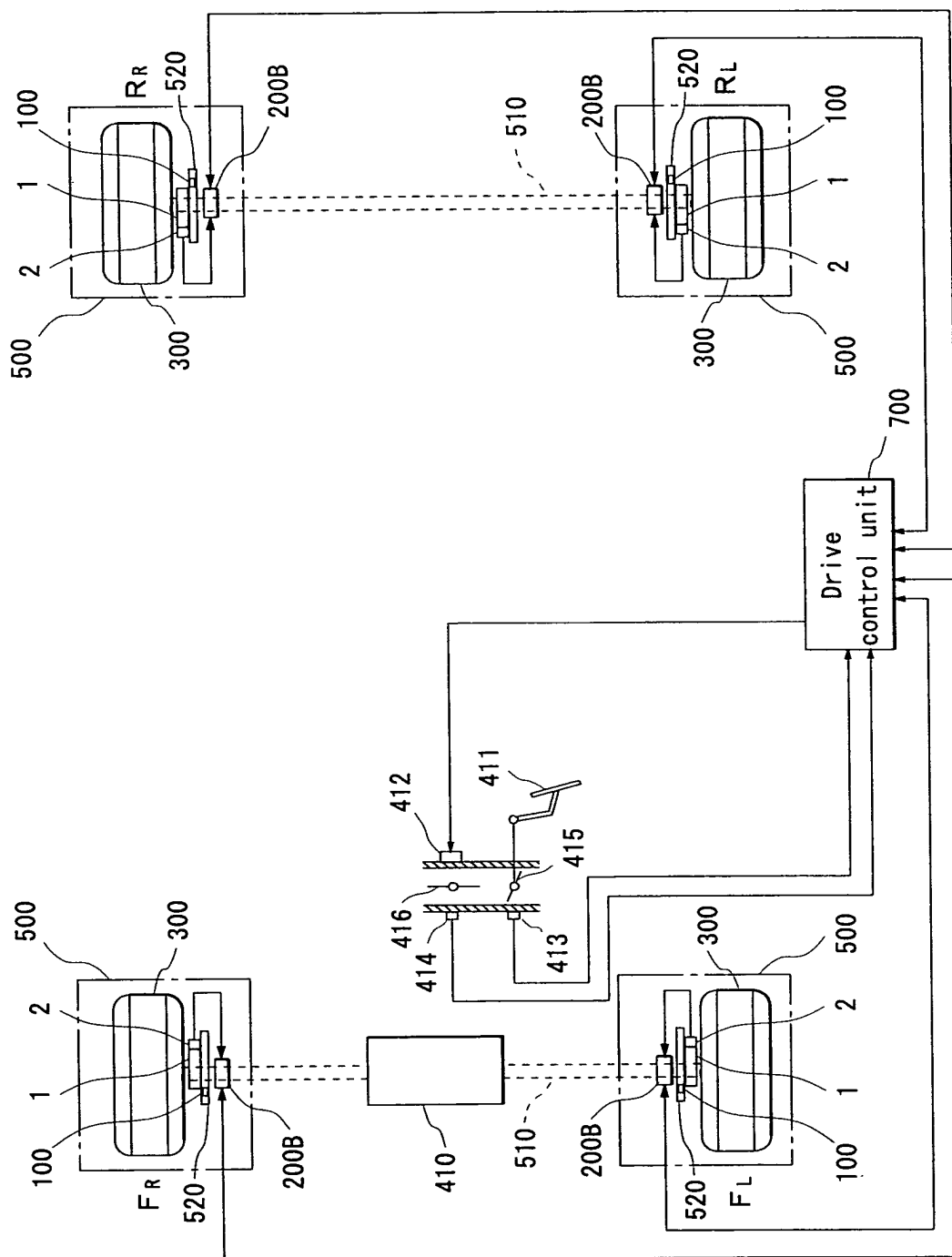
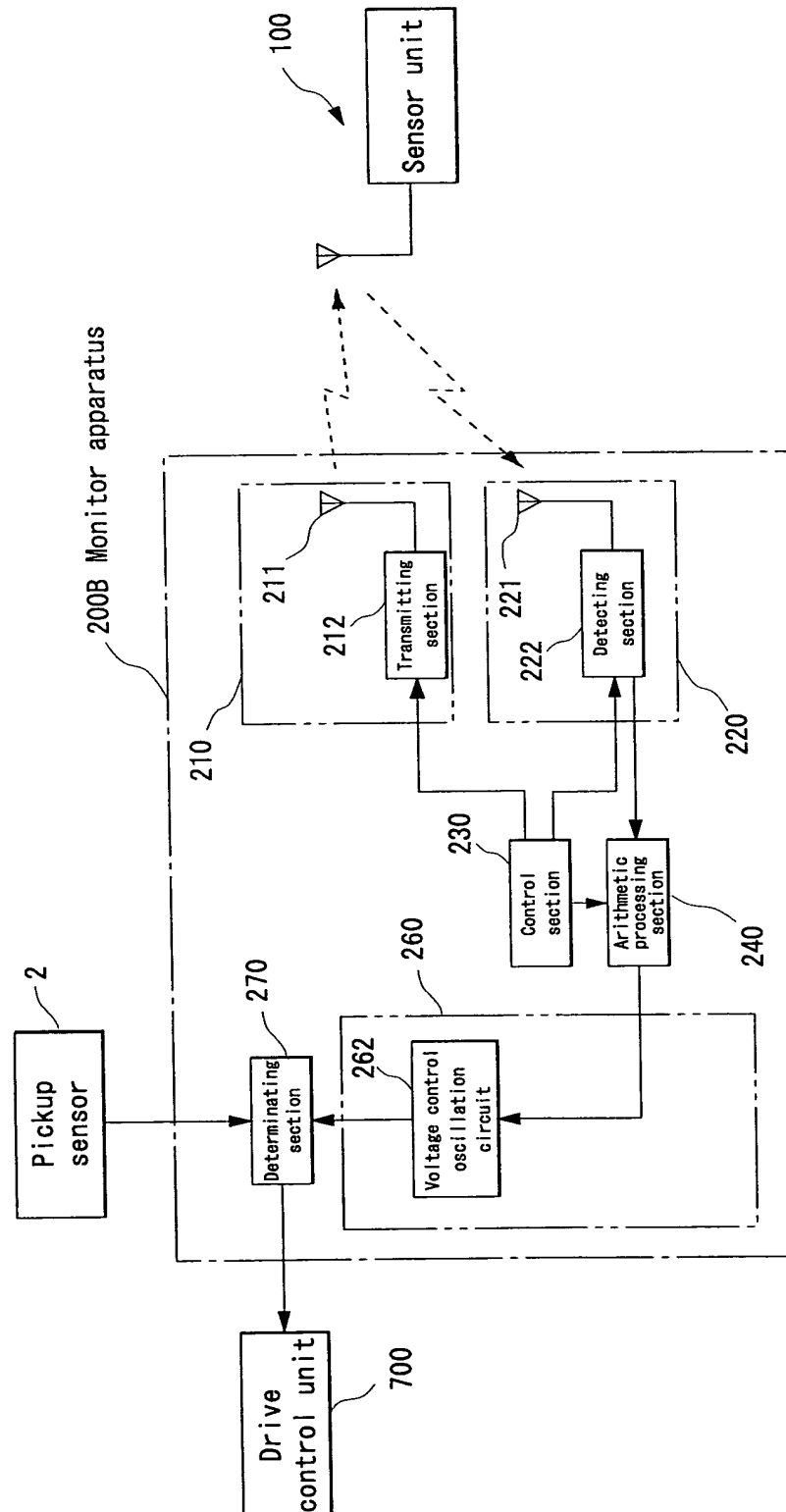


Fig. 44



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Fig. 45

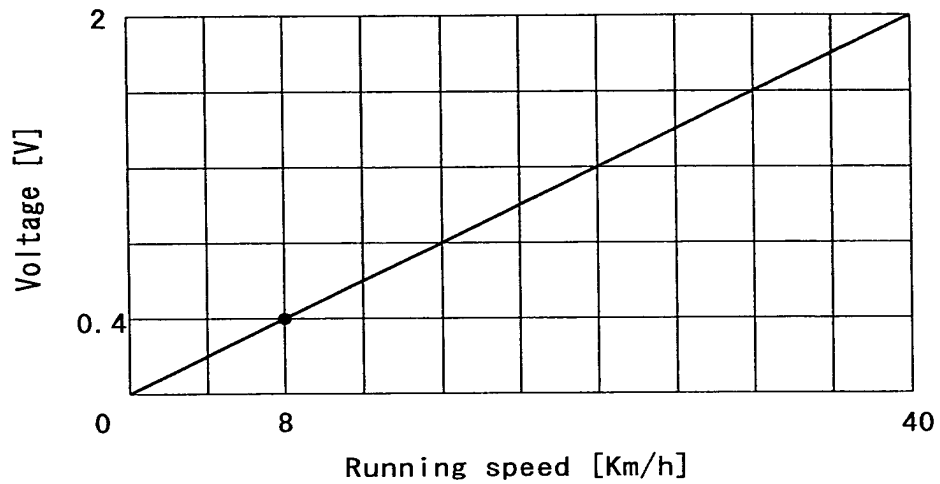


Fig. 46

